



AH3975Q/AH3976Q/AH3977Q/AH3978Q

(Top View)

AUTOMOTIVE DUAL HALL EFFECT LATCH WITH SPEED & DIRECTION OUTPUTS INTEGRATED SELF-DIAGNOSTICS

Description

The AH3975Q/AH3976Q/AH3977Q/AH3978Q is an AEC-Q100 qualified high voltage dual Hall-effect sensor designed for the applications that require accurate speed and direction sensing. To support wide range of demanding applications, the design has been optimized to operate over the supply range of 2.7V to 27V. With chopper stabilized architecture and an internal bandgap regulator to provide temperature compensated supply for internal circuits, the AH3975Q/AH3976Q/AH3977Q provides speed and direction outputs, while AH3978Q provides two independent outputs at Q1 and Q2.

For robustness and protection, the device has a reverse blocking diode with a Zener clamp on the supply. The built-in thermal protection also shuts down the chip if temperature rises to an abnormal value, and it will automatically restart the chip once the junction temperature drops below the safe value. The device is integrated with self-diagnostics which monitor internal blocks, and it will enter safe mode when error is detected.

In the occasion of a supply voltage drop to minimum threshold point, undervoltage lockout protection would be triggered to freeze the device, which prevents the electrical malfunction from affecting the next magnetic measurement circuits, and the output current state updated is always based on the proper accurate measurement result.

Features and Performance

- Dual Latch Hall Operation with Dual Outputs (AH3978Q) or Speed & Direction Output (AH3975Q/AH3976Q/AH3977Q)
- Wide Supply Voltage Operation: 2.7V to 27V
- Chopper Stabilized Design Provides
 - Superior Temperature Stability
 - Minimal Switch Point Drift
 - **Enhanced Immunity to Stress**
- Battery Polarity Reverse Connection Protection
- Transient Spike Voltage Protection
- Overtemperature Shutdown and Auto-Restart
- **UVLO Protection**
- High ESD Rating: HBM = 5kV, CDM = 2kV
- AEC-Q100 Grade 0 Qualified
- Ready for ISO 26262
- Built-in Self-Diagnostics for Functional Safety Requirements
- Temperature Range: -40°C to +150°C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The AH3975Q/AH3976Q/AH3977Q/AH3978Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Pin Assignments

VDD

GND

GND

3

5 DIR/Q1 2 4 SP/Q2

TSOT25 (Type A1)

Applications

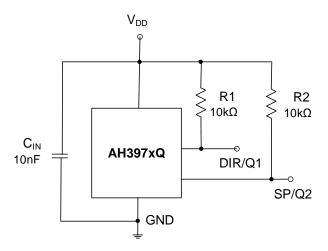
- Power closures with anti-pinch features
- Rotation speed & direction detection
- Linear speed & direction detection
- Angular position detection
- Knob controls

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.



Typical Applications Circuit

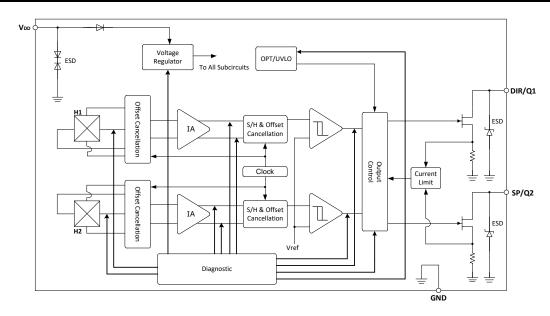


Pin Descriptions

Package: TSOT25 (Type A1)

Pin Number	Pin Na	ame	Function
4	AH3975Q/AH3976Q/AH3977Q	SP	Speed, open-drain output
4	AH3978Q	Q2	Speed 2, open-drain output
F	AH3975Q/AH3976Q/AH3977Q	DIR	Direction, open-drain output
5	AH3978Q	Q1	Speed 1, open-drain output
1	V _D	D	Supply voltage input
2	GN	D	Ground
3	GN	D	Ground

Functional Block Diagram





Absolute Maximum Ratings (Note 4) (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Rating	Unit
V _{DD} (Note 5)	Supply voltage	40	V
V _{DDR} (Note 5)	Reverse supply voltage	-18	V
I _{DD}	Supply current	50	mA
IDDR	Reverse supply current	-50	mA
lout	Output current	50	mA
loutr	Reverse output current	-50	mA
В	Magnetic flux density	Unlimited	GS
TA	Operation ambient temperature	-40 to +150	°C
TJ	Maximum junction temperature	+180	°C
Ts	Storage temperature	-55 to +180	°C
ESD (HBM)	ESD (Human Body Model)	5000	V
ESD (CDM)	ESD (Charged Device Model)	2000	V

Recommended Operating Conditions (@TA = -40°C to +150°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DD}	Supply voltage	Operating	2.7	27	V
Тор	Operating temperature	Operating	-40	+150	°C

^{4.} Stresses greater than those listed under Absolute Maximum Ratings can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability.

5. Should not be exceeding the maximum junction temperature and maximum duration of 500ms.





Electrical Characteristics (Note 6) (@TA = -40°C to +150°C, VDD = 2.7V to 27V, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V _{DD}	Supply voltage	Operating		12	27	V
IDD	Supply current	V _{DD} = 2.7V to 27V	3.5	4.7	7	mA
Vuvlo	Undervoltage lockout threshold	V _{DD} falling	2.0	2.35	2.7	V
IDDR	Reverse supply current	$V_{DD} = -18V$, $T_A = -40^{\circ}C$ to $+150^{\circ}C$	-1.5		1	mA
Vosat	Output saturation voltage	B > Bop, Iout = 10mA	ı	0.2	0.5	V
ILEK	Output leakage current	Vout = 12V, V _{DD} = 12V, B < B _{RP}	_	0.1	1	μA
I _{LIM}	Output current limit	Output on	11	25	44	mA
T _{TP}	Thermal protection threshold	Junction temperature	+165	+180	_	°C
T _{TPR}	Thermal protection release threshold	Junction temperature	_	+155	_	°C
fM	Maximum switching frequency*	B > 3 x Bop, alternative square magnet field	40	60	ı	kHz
fc	Chopping frequency*	_	_	500	_	kHz
tpon	Power on time (Note 7)	$V_{DD} = 12V$, $dV_{DD}/dt > 2.7V/\mu s$	_	13	_	μs
t _R	Output rise time*	$V_{DD} = 12V$, pullup resistor $1k\Omega$, $C_L = 50pF$	1	0.2	1	μs
tF	Output fall time*	$V_{DD} = 12V$, pullup resistor $1k\Omega$, $C_L = 50pF$	_	0.2	1	μs
t _D	Response delay time (Note 8)	B > 3 x Bop	_	13	_	μs
t _{DC}	Count signal delay (Note 6)	_	50	400	1000	ns
tu	Output jitter (Note 6)	_	_	±5	_	µsrмs
dhall	Hall plate distance	_	_	1.45	_	mm
Vz	Zener clamp voltage	I _{DD} = 8mA, T _A = +25°C	36	_		V

^{*} Guaranteed by design.

^{6.} Typical values are defined at T_A = +25°C, V_{DD} = 12V. Maximum and minimum values over the operating temperature range are not tested in production but guaranteed by design, process control and characterization.

^{7.} Time from applying $V_{DD} \ge 2.7V$ to the sensor until the output state is valid. 8. Time delayed from the magnetic threshold reached to the output rise or fall.

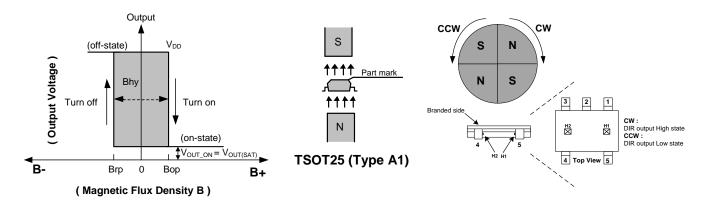


Magnetic Characteristics (Notes 6, 9) (TA = -40°C to +150°C, VDD = 2.7V to 27V, unless otherwise specified.)

Part Name	•	rating F P (Gaus			ease Po			ysteres vs (Gau		Mato (Ga	netic hing uss) e 10)	Off	netic set uss) e 10)	TC (ppm/°C)		Out	put	
	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Max	Min	Max	Тур	SPD	DIR	Q1	Q2
AH3975Q	-10	10	30	-30	-10	10	5	20	35	-25	25	-15	15	-350	V	V	_	_
AH3976Q	8	25	42	-42	-25	-8	32	50	68	-20	20	-20	20	-350	V	V	_	_
AH3977Q	50	75	100	-100	-75	-50	120	150	180	-30	30	-20	20	-350	V	V		_
AH3978Q	50	75	100	-100	-75	-50	120	150	180	-30	30	-20	20	-350	_	_	V	V

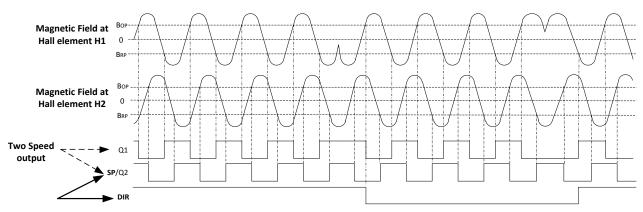
Notes:

- 9. Positive x-axis direction indicates the south pole approaching the part marking surface i.e. increasing south pole magnetic field strength to the sensor; reversing direction x-axis toward 0 means the decreasing south magnetic field strength to the sensor. Negative x-axis indicates north pole magnetic field to the part marking surface.
- 10. $T_A = +25$ °C, $V_{DD} = 2.7$ V to 27V.

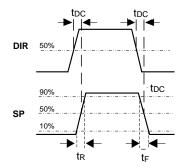


Operating Characteristics

Timing Diagrams for the Speed and Direction Output SP/DIR and Two Speed Outputs Q1/Q2

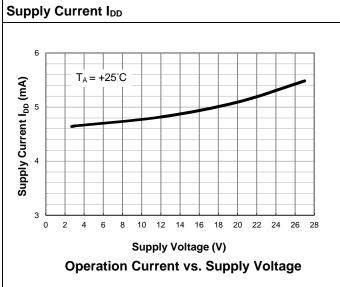


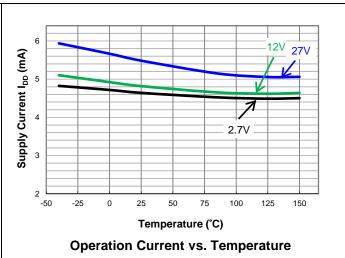
Count Signal Delay toc



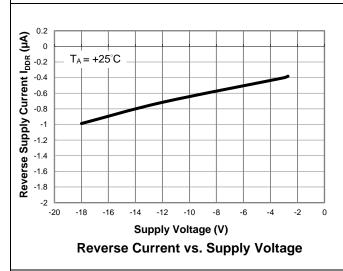


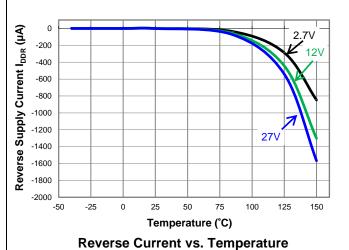
Performance Characteristics



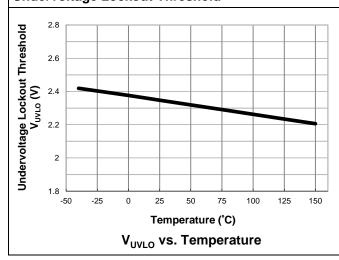


Reverse Supply Current IDDR

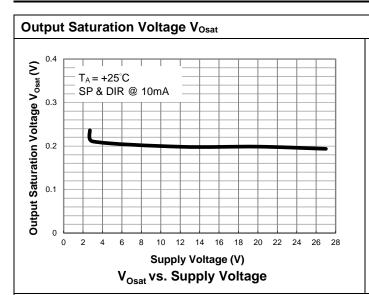


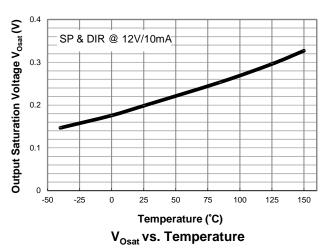


Undervoltage Lockout Threshold

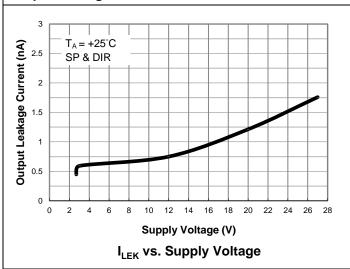


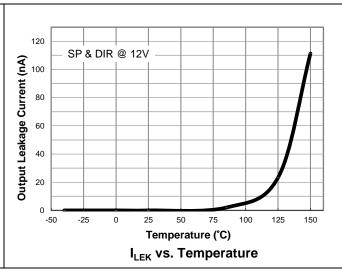




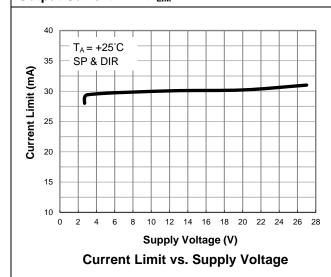


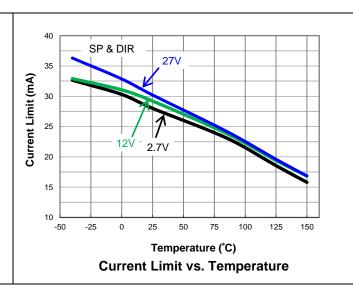
Output Leakage Current ILEK



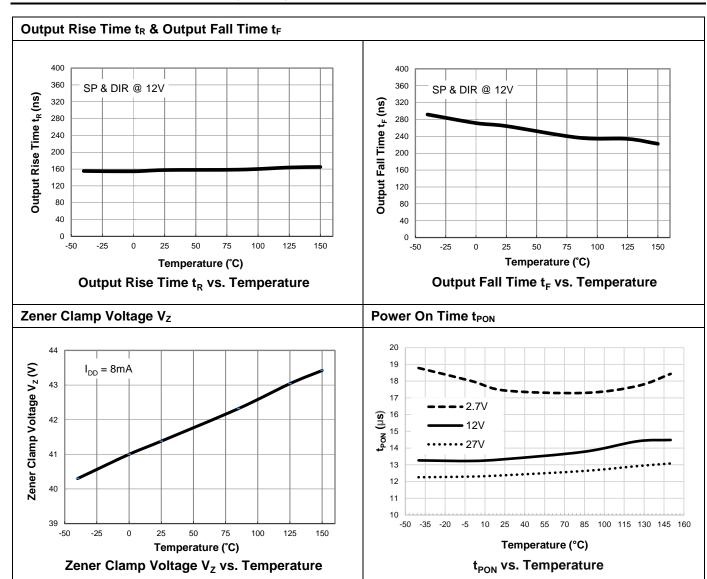


Output Current Limit ILIM

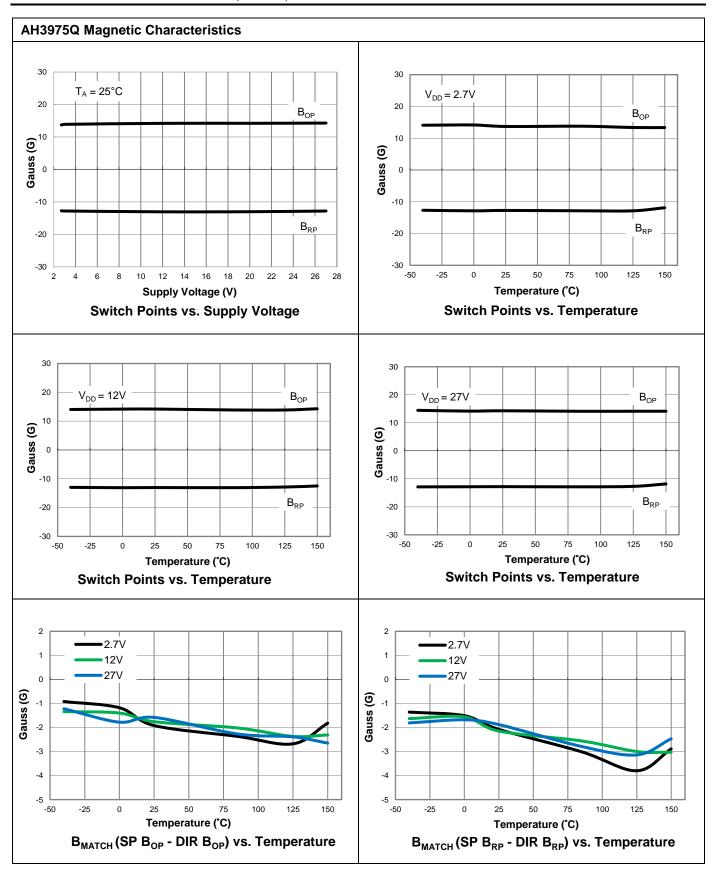




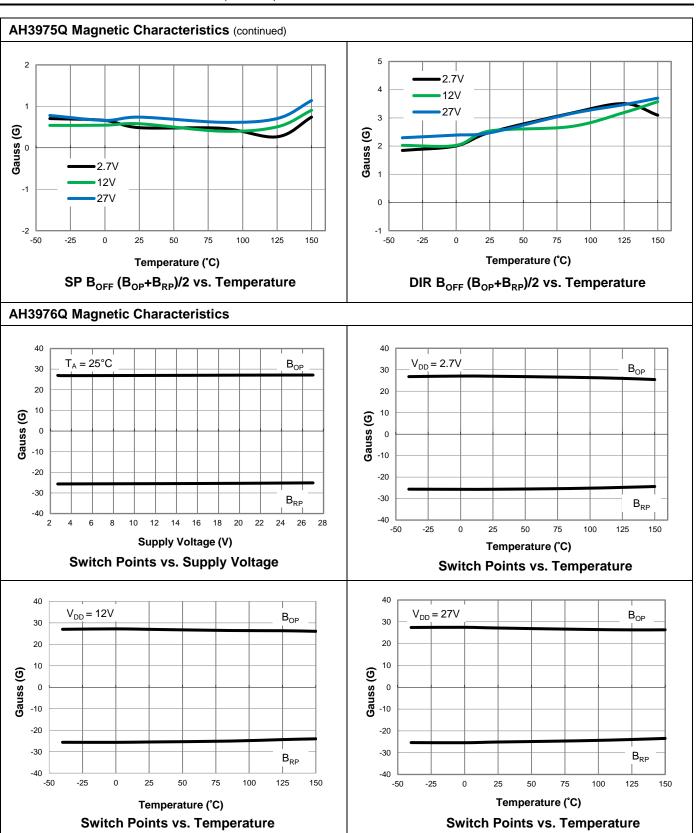




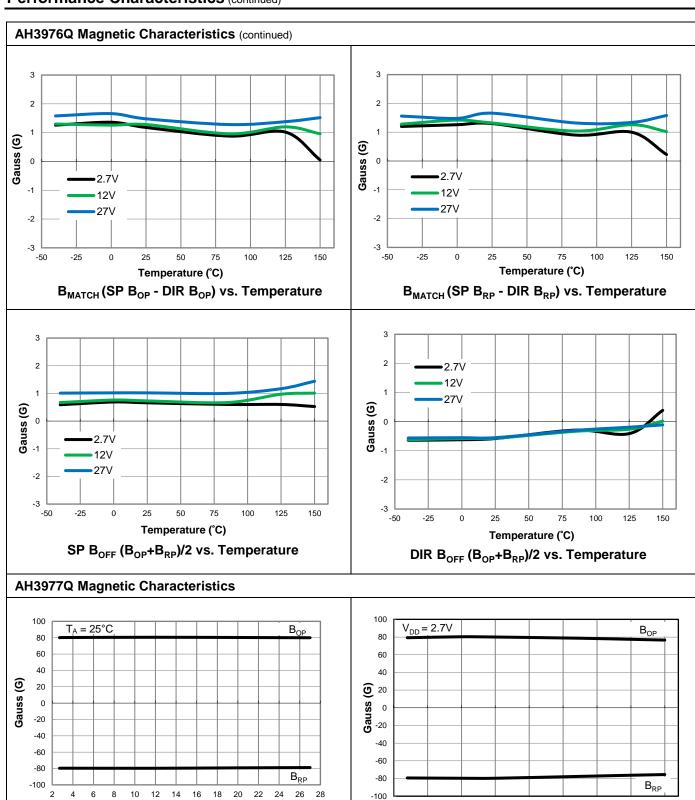












Supply Voltage (V)

Switch Points vs. Supply Voltage

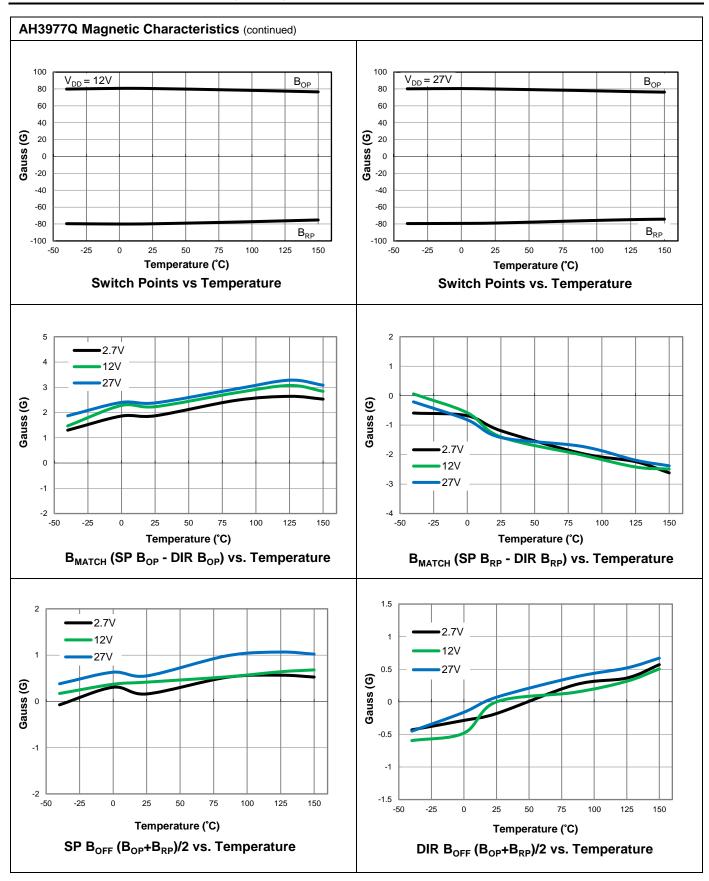
-50

-25

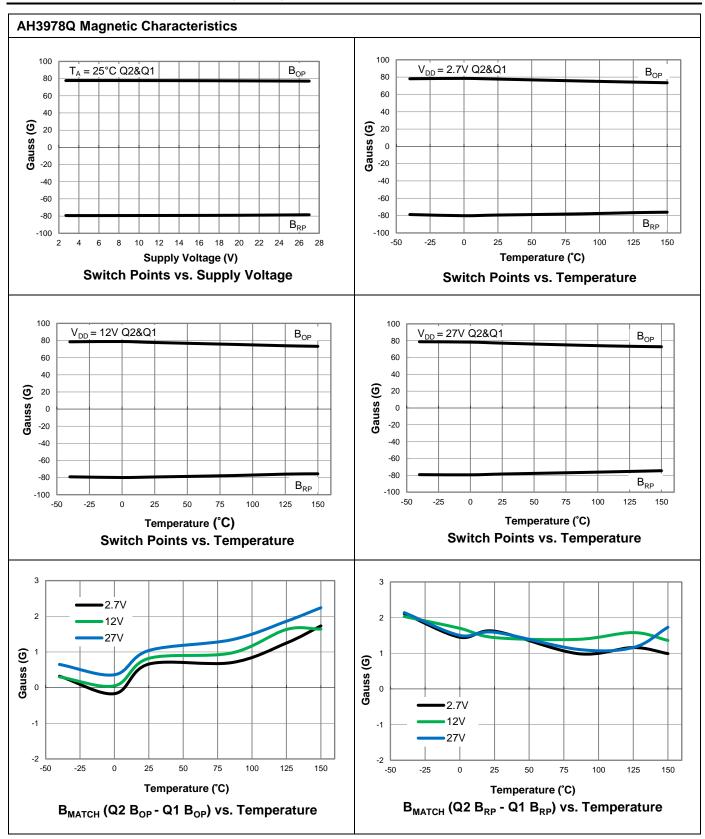
Temperature (°C)

Switch Points vs. Temperature

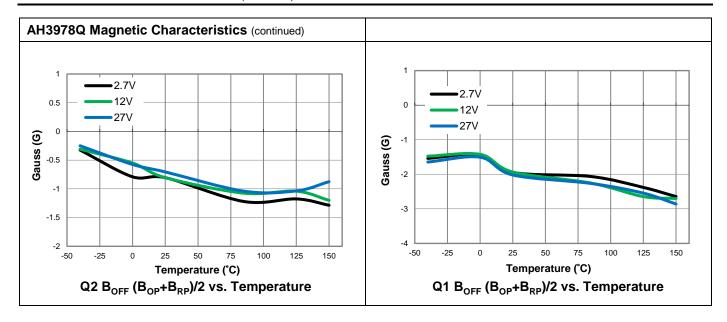






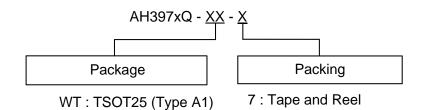








Ordering Information



Part Number	Part Number Suffix	Package Code	Package	Packing				
Fait Number	Part Number Sumx	Package Code	Fackage	Qty.	Carrier			
AH3975Q-WT-7	-7	WT	TSOT25 (Type A1)	3000	7" Tape and Reel			
AH3976Q-WT-7	-7	WT	TSOT25 (Type A1)	3000	7" Tape and Reel			
AH3977Q-WT-7	-7	WT	TSOT25 (Type A1)	3000	7" Tape and Reel			
AH3978Q-WT-7	-7	WT	TSOT25 (Type A1)	3000	7" Tape and Reel			

Marking Information

Package Type: TSOT25 (Type A1)

(Top View)

5 4 XXX2 3

XXX: Identification Code

 \underline{Y} : Year 0 to 9 (ex: 3 = 2023) W: Week: A to Z: week 1 to 26;

a to z : week 27 to 52; z represents week 52 and 53 $\,$

X: Internal Code

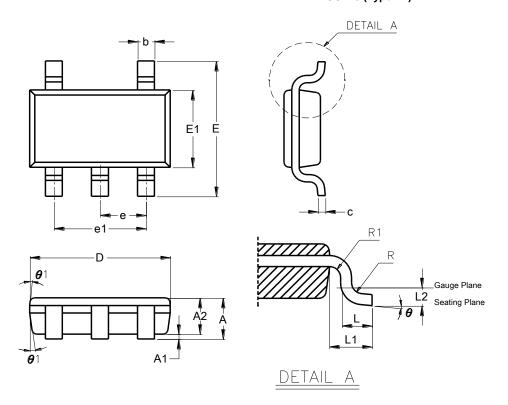
Part Number	Package	Identification Code
AH3975Q-WT-7	TSOT25 (Type A1)	M6Q
AH3976Q-WT-7	TSOT25 (Type A1)	M7Q
AH3977Q-WT-7	TSOT25 (Type A1)	M8Q
AH3978Q-WT-7	TSOT25 (Type A1)	M9Q



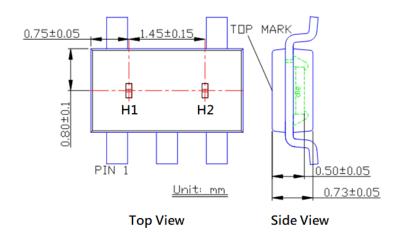
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT25 (Type A1)



TSOT25 (Type A1)							
Dim	Min Max Typ						
Α	0.750	0.800					
A1	0.00	0.050					
A2	0.700	0.775	0.750				
b	0.350	0.500					
С	0.100	0.200					
D	2.800	3.000	2.900				
E	2.600	3.000	2.800				
E1	1.500	1.700	1.600				
е	C	.950 BS	C				
e1	1	.900 BS	SC				
L	0.370	0.600	0.450				
L1	C	.600 RE	F				
L2	0	.250 BS	C				
R	0.100						
R1	0.100						
θ	0°	8°	4°				
θ1	4°	12°	10°				
All [Dimens	ions in	mm				



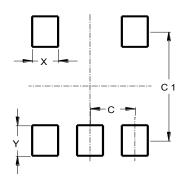
Sensor Location



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT25 (Type A1)



Dimensions	Value (in mm)
С	0.95
C1	2.50
Х	0.55
Y	0.70

Mechanical Data

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.016 grams (Approximate)



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